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APPLICANTS

Keishi Saito, Nara-ken, JAPAN;

Masafumi Sano, Kyoto, JAPAN;

** CONTINUING DATA *****

This application is a DIV of 09/839,891 04/23/2001 PAT 6,635,899
 which is a DIV of 09/266,829 03/12/1999 PAT 6,303,945

** FOREIGN APPLICATIONS *****

JAPAN 10-065287 03/16/1998

JAPAN 10-066854 03/17/1998

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/24/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

ADDRESS

5514
 FITZPATRICK CELLA HARPER & SCINTO
 30 ROCKEFELLER PLAZA
 NEW YORK, NY
 10112

TITLE

Semiconductor element and its manufacturing method

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
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